

From: [Apon, Daniel C. \(Fed\)](#)
To: [Alperin-Sheriff, Jacob \(Fed\)](#); [Moody, Dustin \(Fed\)](#); [Perlner, Ray A. \(Fed\)](#); (b) (6); [Bassham, Lawrence E. \(Fed\)](#)
Subject: RE: Teams specifically requesting hardware support
Date: Thursday, February 21, 2019 12:58:06 PM

Teams that have no expertise in or ability to produce their own hardware implementations, wanting someone else to do it

(Since we have at least 3 groups—NYU, UofA, PriVerify – and hopefully, some more coming after my FPGA trip, we can make sure those hardware groups implement the given algorithms)

From: Alperin-Sheriff, Jacob (Fed)
Sent: Thursday, February 21, 2019 11:48 AM
To: Apon, Daniel C. (Fed) <daniel.apon@nist.gov>; Moody, Dustin (Fed) <dustin.moody@nist.gov>; Perlner, Ray (Fed) <ray.perlner@nist.gov>; Daniel Smith (b) (6)
Subject: Re: Teams specifically requesting hardware support

Elaborate what does additional hardware support mean?

From: "Apon, Daniel C. (Fed)" <daniel.apon@nist.gov>
Date: Thursday, February 21, 2019 at 10:52 AM
To: "Moody, Dustin (Fed)" <dustin.moody@nist.gov>, "Perlner, Ray (Fed)" <ray.perlner@nist.gov>, "Alperin-Sheriff, Jacob (Fed)" <jacob.alperin-sheriff@nist.gov>, Daniel Smith (b) (6)
Subject: Teams specifically requesting hardware support

Hi guys,

FYI.. so far,

Three Bears, NTS-KEM, and GeMSS have each contacted me, specifically asking for additional hardware support (and offering to answer questions for any hardware teams that are involved in implementing their candidates)

--Daniel A